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Our Case No. 9281-3561

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PATENT APPLICATION TRANSMITTAL LETTER

The Assistant Commissioner for Patents:

Transmitted herewith for filing is the patent application of Takayuki Sugawara, Hiroki Hirashima, Isao Asano and Kyoichi Shirane

for Disk Device with Improved Error-Correction Capability

Enclosed are:

19 pages of application (including title page), 3 sheet(s) of drawings and the following Appendices : n/a.

Preliminary Amendment

Declaration and Power of Attorney for Patent Application

Assignment transmittal letter and Assignment of the invention to : Alps Electric Co., Ltd. and Sony Corporation.

Submission of Certified Copy of Priority Document and certified copy of Japanese Application No. 11-047937.

Information Disclosure Statement; PTO-1449 reference A1.

Claims as Filed	Col. 1	Col. 2
For	No. Filed	No. Extra
Basic Fee		\$ 345
Total Claims	3-20	0
Indep. Claims	1-3	0
Multiple Dependent Claims Present		

*If the difference in col. 1 is less than zero, enter "0" in col. 2.

Small Entity	
Rate	Fee
x\$9=	\$
x\$39=	\$
+\$130=	\$
Total	\$

Other Than Small Entity	
Rate	Fee
x\$18=	\$ 690
x\$78=	\$ 0
+\$260=	\$ 0
Total	\$690

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Any additional filing fees required under 37 CFR § 1.16.

Any patent application processing fees under 37 CFR §1.17.

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Any filing fees under 37 CFR § 1.16 for presentation of extra claims.

Any patent application processing fees under 37 CFR § 1.17.

The issue fee set in 37 CFR § 1.18 at or before mailing of the Notice of Allowance, pursuant to 37 CFR § 1.311(b).

Date

2/24/66

Gustavo Siller, Jr.
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Registration No. 32,805

Our Case No. 9281-3561
Client Reference No. J US98110/S00P0376US00J

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
APPLICATION FOR UNITED STATES LETTERS PATENT

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TITLE: Disk Device with Improved Error-
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DISK DEVICE WITH IMPROVED ERROR-CORRECTION CAPABILITY

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to disk devices in which error-correction processing is performed when an error occurs during reading of data from disks.

2. Description of the Related Art

Fig. 4 illustrates error-correction processing performed by a conventional disk device.

A disk device shown in Fig. 4 is used for reading data from and writing data to a conventional flexible disk. A disk drive 10 and a host computer 30 are connected by a predetermined interface. The disk drive 10 loads a disk 16 having a predetermined format in a disk drive body 14. The host computer 30 includes an operating system (OS) 31 and a device driver 32 for controlling the disk drive body 14. Data delivery is performed between the disk drive body 14 and the OS 31 by means of the device driver 32.

An error-correction code (hereinafter referred to as an "ECC"), such as a C1 code of Reed Solomon codes, is added along with data itself to a signal written to the disk 16 for every sector. Error correction is performed by reading and processing the ECC when reading data.

The conventional disk device is provided with an error-correction unit on a processing circuit of the disk drive 10. Using the ECC, such as the C1 code, read from the disk, the error-correction unit in the disk drive 10 performs error correction in units of one block (physical block address; PBA) corresponding to data in one sector.

A flexible magnetic disk for high-density storage with a capacity of, for example, approximately 200 MB, has a substantially smaller track width than a conventional disk due to increased writing density. The disk for high-density storage has a much higher writing frequency. When an error occurs in read data due to a defect or dirt on the surface of the disk, the size of the region influenced by such a defect or dirt is substantially increased, so as to extend over a plurality of sectors. In such a case, an error may not be recovered by error correction in data units (PBA units) for one sector based on the ECC, such as the C1 code.

It has therefore been proposed to provide a hardware error-correction unit having an error correction capability higher than the above error-correction unit. In order to provide such an error-correction unit, a CPU contained in the disk drive 10 must be replaced by one with a greater operational capacity. It is also necessary to increase the memory capacity. Thus, an electronic circuit installed in the disk drive 10 must be very expensive.

SUMMARY OF THE INVENTION

Accordingly, it is an object of the present invention to provide a disk drive having an error-correction capability higher than that of a conventional device without modifying the disk drive hardware.

According to one aspect of the present invention, there is provided a disk device including a disk drive including a head for reading data written to a disk and a processing circuit for processing the data, and a host computer connected to the disk drive through an interface. The processing circuit of the disk drive includes a low-level error-correction unit for performing error correction of the data written to the disk in units of one block. The host computer includes a high-level error-correction unit for performing error correction of the read data supplied through the interface in units of a plurality of blocks.

For example, the low-level error-correction unit performs error correction for every block, where one block corresponds to one sector in which one piece of data is written. Data in every block are grouped as a plurality of blocks, and the high-level error correction performs error correction for every plurality of blocks.

Accordingly, when an error that extends over sectors is caused by a defect or dirt on the surface of a disk, which

has been uncorrectable hitherto, it is possible to recover data read from the disk. In such a case, the high-level error-correction unit may be simply obtained by installing software in the host computer. It is therefore unnecessary to implement hardware modifications, such as building a new arithmetic circuit on the disk drive circuit, increasing the capability of a CPU, and increasing the storage capacity.

A high-reliability disk to which both a low-level ECC and a high-level ECC are written and a disk to which only the low-level ECC is written may be loaded into the disk drive. When the high-reliability disk is loaded, the processing circuit of the disk drive performs low-level error correction, and then the host computer, to which the corrected data is supplied, performs high-level error correction. When the latter disk is loaded, the processing circuit of the disk drive performs low-level error correction, and the host computer processes the corrected data.

For example, the high-reliability disk includes a SYNC region, a data region, and a low-level ECC (for example, a C1 code) for every sector. The high-reliability disk also includes a high-level ECC (for example, a C2 code) for every plurality of sectors. In contrast, only a low-level ECC (for example, a C1 code) is written to the latter disk. When the high-reliability disk is loaded into the disk drive,

the processing circuit of the disk drive performs error correction based on the low-level ECC. The corrected data is supplied to the host computer, and the host computer performs error correction based on the high-level ECC in units of blocks (logical block address; LBA). When the disk other than a high-reliability type is loaded, the disk drive only performs error correction based on the low-level ECC. The corrected data is supplied to the host computer.

When writing data to the high-reliability disk, the host computer processes a low-level ECC for every sector and adds the low-level ECC to the data. In addition, the host computer processes a high-level ECC for every plurality of sectors and adds the high-level ECC to the data.

Information may be written to a disk for discriminating a high-reliability disk to which both a low-level ECC and a high-level ECC are written from a disk to which only the low-level error correction is written. The host computer may determine which disk is inserted based on the information.

Specifically, there is no difference between formats to be written to disks, discriminating a disk to which both the low-level ECC and the high-level ECC are written from a disk to which only the low-level ECC is written. Thus, it is necessary to discriminate between both disks prior to reading from or writing to the disk. In this case, it is

useful to write identification numbers of both disks in a region in the disk which is inaccessible to a user.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a circuit block diagram of a disk device according to an embodiment of the present invention;

Fig. 2 is an illustration of a method of error-correction processing performed by the disk device shown in Fig. 1;

Fig. 3 is an illustration of an ECC format and error-correction processing; and

Fig. 4 is an illustration of a method of error-correction processing performed by a conventional disk device.

DESCRIPTION OF THE PREFERRED EMBODIMENT

A disk device according to an embodiment of the present invention is described with reference to the accompanying drawings. Fig. 1 is a block diagram of a disk drive 10 of the disk device according to the embodiment of the present invention. Fig. 2 is an illustration of a method of error-correction processing. Fig. 3 is an illustration of error-correction processing.

The embodiment described hereinafter employs a flexible magnetic disk for high-density storage with a memory

capacity of, for example, 200 MB, which is loaded into the disk drive 10.

Referring to Fig. 1, a disk drive body 14 in the disk drive 10 is provided with a rotational driver 1. The rotational driver 1 includes a turntable 3 which is rotatably-driven by a spindle motor 2. The center of a disk D is clamped on the turntable 3. The disk D is a flexible disk for high-density storage. The disk D is inserted from the outside into the disk device to be mounted on the rotational driver 1. The spindle motor 2 is driven and controlled by a motor driver 4.

A magnetic head H0 opposing a side #0 of the disk D and a magnetic head H1 opposing a side #1 of the disk D are supported by a support arm 5 on a head base 6. The head base 6 is driven by a voice coil motor (VCM) 7, i.e., a linear motor driver, in the radial direction of the disk D. The VCM 7 is driven and controlled by a VCM driver 8.

The motor driver 4 and the VCM driver 8 are activated by control signals from a CPU 9 as a controller.

The magnetic heads H0 and H1 are connected to a read/write (R/W) amplifier 11. The R/W amplifier 11 is connected to a disk controller 13. The disk controller 13 is connected via a bus to a host computer 20 through an input/output interface (see Fig. 2). The disk drive body 14 also includes an error-correction unit 12.

A write signal from the host computer 20 is formatted by the disk controller 13. The error-correction unit 12 processes data and adds an ECC to the data, which is then supplied to the magnetic heads H0 and H1 through the R/W amplifier 11. The magnetic heads H0 and H1 read a signal from the disk D, which in turn is supplied from the R/W amplifier 11 to the disk controller 13 to be un-formatted. The read data is supplied to the host computer 20 through a bus in logical block address (LBA) units. The error-correction unit 12 in the disk drive body 14 performs low-level error correction.

Referring now to Fig. 2, the host computer 20 includes therein an OS 21 and a device driver 22 for controlling the disk drive 10. The disk drive 10 includes the disk drive body 14.

A high-reliability disk 15, which is a flexible magnetic disk for high-density storage with a memory capacity of, for example, 200 MB, in which predetermined bits are redundancy bits for high-level error correction, and a disk 16, which also has a memory capacity of 200 MB and to which only a low-level ECC is added, are compatible for loading into the disk drive body 14.

Prior to reading/writing of data, the disk drive body 14 confirms the disk type. This is performed either by a unit for reading a disk-type identification signal written

in a predetermined region of a loaded disk and then notifying the host computer 20, or a unit for issuing an appropriate command (for example, a mode-sense command or a "vender-unique" command in accordance with this object) from the host computer 20. Based on the obtained information, the host computer 20 determines whether it is necessary to write data to which the high-level ECC is added (or to read data as if the high-level ECC is added to the data) or to read/write data to which only the low-level ECC is added.

Fig. 3 schematically illustrates a writing format of the high-reliability disk 15. The writing format of the high-reliability disk 15 is such that, for every sector, a SYNC region 23, a region storing data 24, and a region storing a low-level ECC (C1 code) 25 are continuously arranged. The low-level ECC (C1 code) 25 is capable of correcting an error in units of one block (physical block address; PBA) corresponding to data in one sector. For example, the amount of data in one block is 512 Bytes.

A high-level ECC (C2 code) 26 for two sectors is written as redundancy bits every 64 sectors. The high-level ECC 26 is capable of performing high-level error correction in the host computer 20 in units of 64 blocks (logical block address; LBA).

The SYNC region 23 in each sector represents the beginning of the data 24. Synchronization is obtained based

on the SYNC region 23, and the data 24 is read.

Although not shown in the drawings, servo information for tracking servo, such as the SYNC region, A burst, and B burst, is written in each sector or a set of sectors in order to correct positional deviations of the magnetic head relative to the disk.

Processing operations performed when the high-reliability disk 15 is loaded into the disk drive body 14 and the magnetic head H0 or H1 reads a signal written to the disk are described below.

A signal read by the magnetic head H0 or H1 is amplified by the R/W amplifier 11, which in turn is sent to the disk controller 13 to be demodulated. Simultaneously, the error-correction unit 12 performs low-level error correction. Specifically, the error-correction unit 12 provided in the disk drive body 14 performs the low-level error correction (C1 correction) based on the C1 code 25. The low-level error correction is performed in data units of one block (PBA) corresponding to the amount of data written in one sector.

The data for which the low-level error correction is performed is sent from the disk controller 13 to the host computer 20 through the input/output interface and a bus line L2. The host computer 20 sends the data for which the low-level error correction is performed to the device driver

22. The device driver 22 performs predetermined data processing and high-level error correction. Then, the data is sent to the OS 21.

The data is transferred from the disk drive body 14 to the host computer 20 in one block (LBA) having, for example, the amount of the data 24 written in one sector. After loading data for 64 blocks (LBAs), the device driver 22 simultaneously reads the high-level ECC (C2 code) for two sectors (LBAs). The C2 correction based on the C2 code for two LBAs is performed in data units of 64 blocks (LBAs). The corrected data is notified to the OS 21.

Even when a reading failure occurs over a plurality of sectors due to, for example, a defect or dirt on the surface of the disk, it is possible to recover, using high-level error correction based on the high-level ECC (C2 code) 26, data that cannot be recovered by error correction based on the C1 code 25.

When the disk 16 to which only the C1 code 25 is written is loaded into the disk drive 10 and a signal is read therefrom, the error-correction unit 12 provided in the disk drive body 14, as shown in Fig. 1, performs low-level error correction based on the C1 code. The corrected data is sent to the device driver 22 through a bus line L1 and the input/output interface. This in turn is processed and notified to the OS 21.

Specifically, the device driver 22 may perform data processing of both the data read from the high-reliability disk 15 having the C1 code 25 and the C2 code 26 and the data read from the disk 16 having only the C1 code 25.

When the error-correction unit 12 of the disk drive body 14 performs error correction using the C1 code, a predetermined number, such as 32, of retry operations are performed until the corrected data is recovered to a predetermined rate. Specifically, when the error rate of the read data is high, the data is repeatedly read until the data on the same sector can be recovered.

If the data is not recovered even after the predetermined number of retry operations, the retrying is terminated. When the loaded disk is the disk 16, which is not a high-reliability type, upon termination of the retrying it is established that data reading is impossible. This is notified to the device driver 22 of the host computer 20.

In contrast, when the loaded disk is the high-reliability disk 15, even when data cannot be recovered after the predetermined number of retry operations, the data is sent to the device driver 22 of the host computer 20. As described above, the high-level error correction using the C2 code 26 is performed. When the data cannot be recovered even with this error correction, a reading failure occurs.

Since the ECC (C2 code) 26 for two sectors is added to the high-reliability disk 15 every 64 sectors, the region available for a user is smaller than that of the disk 16, which is not a high reliability type. For example, when the disk 16 has a 200 MB memory capacity, the memory capacity (redundancy) required for the C2 code 26 is approximately 6 MB ($= 200 \text{ MB} \times 2 / (64 + 2)$). The region available to the user is therefore decreased to approximately 194 MB.

The capacity of the C2 code 26 can be changed, as necessary, according to the required disk reliability. For example, when increasing reliability, the capacity is changed by increasing redundancy. This may be performed by adding a C2 code of more than two sectors or by adding a C2 code fewer than every 64 sectors.

When the redundancy is changed as described above, disks with different redundancies have different processing modes. It is therefore necessary to provide different disk-type identification information to be written on the disks.

When the disk device writes data to a disk, the data is sent from the OS 21 to the device driver 22, and is then transferred to the disk drive 10 through the bus line L2 in LBA units. Simultaneously, the device driver 22 processes the data in units of, for example, 64 LBAs, and generates the C2 code 26. After transferring the data for 64 LBAs, the C2 code 26 for two LBAs is sent to the disk drive body

14 through the bus line L2.

The disk drive body 14 performs predetermined formatting, such as for the SYNC region 23 and the data 24, in one PBA unit. Simultaneously, the error-correction unit 12 processes the data in one PBA as one unit and generates the C1 code 25. The generated C1 code is added to the data, which in turn is written to the disk by means of the magnetic heads H0 and H1.

Accordingly, the disk device is capable of forming the high-reliability disk 15.

When the disk device writes data to the disk, the C2 code 26 may not be added, and only the C1 code 25 may be added, thus forming the disk 16.

When the high-reliability disk 15 or the disk 16 is formed, a specific disk-type identification signal is written in a predetermined region.

The disk device according to the present invention is not limited to the above embodiment. The present invention is not limited to disk device for reading/writing the above flexible magnetic disk for high-density storage, and alternatively, may be applied to a disk device for reading/writing another disk, such as an optical disk or a magneto-optical disk. Changes and variations may be made without departing from the spirit of the invention. For example, it is possible to change the space between the C2

codes 26 or the memory capacity to be allocated.

WHAT IS CLAIMED IS:

1. A disk device comprising:

a disk drive including a head for reading data written to a disk and a processing circuit for processing the data; and

a host computer connected to said disk drive through an interface;

wherein the processing circuit of said disk drive includes a low-level error-correction unit for performing error correction of the data written to the disk in units of one block; and

the host computer includes a high-level error-correction unit for performing error correction of the read data supplied through the interface in units of a plurality of blocks.

2. A disk device according to Claim 1, wherein:

a high-reliability disk to which both a low-level error-correction code and a high-level error-correction code are written and a disk to which only the low-level error-correction code is written are loadable into said disk drive;

when the high-reliability disk is loaded, the processing circuit of said disk drive performs low-level

error correction, and then said host computer, to which the corrected data is supplied, performs high-level error correction; and

when the latter disk is loaded, the processing circuit of said disk drive performs low-level error correction, and said host computer processes the corrected data.

3. A disk device according to Claim 1, wherein:

information is written to the disk for discriminating a high-reliability disk to which both a low-level error-correction code and a high-level error-correction code are written from a disk to which only the low-level error correction is written; and

said host computer determines which disk is inserted based on the information.

ABSTRACT OF THE DISCLOSURE

A disk device for performing error-correction processing is loaded with a high-reliability disk to which a C1 code as a low-level error-correction code (ECC) is added for every sector, and a C2 code as a high-level ECC is added for every plurality of sectors. A host computer includes a device driver corresponding to the high-reliability disk. When data written to the high-reliability disk is read, low-level error-correction is performed based on the C1 code in units of one block corresponding to one sector, and the corrected data is notified to the host computer. Simultaneously, the C2 code along with the data for a plurality of blocks is loaded, and high-level error correction based on the C2 code is thereby performed.

FIG. 1

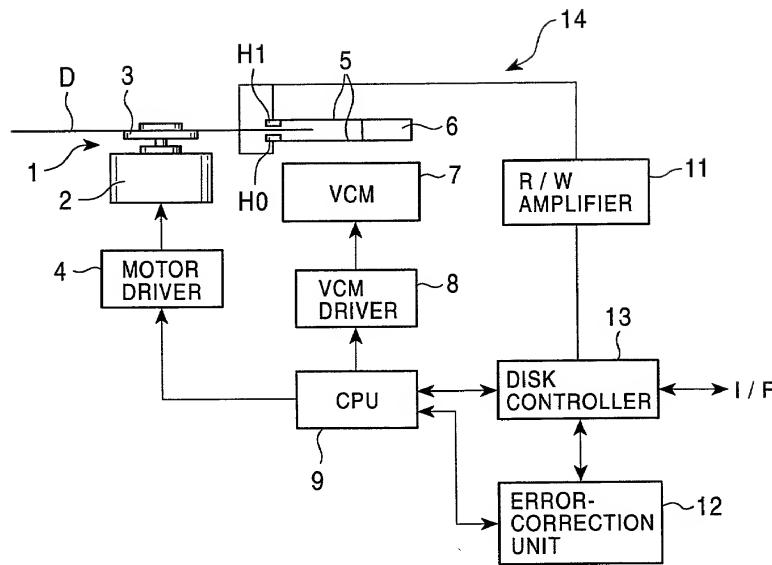


FIG. 2

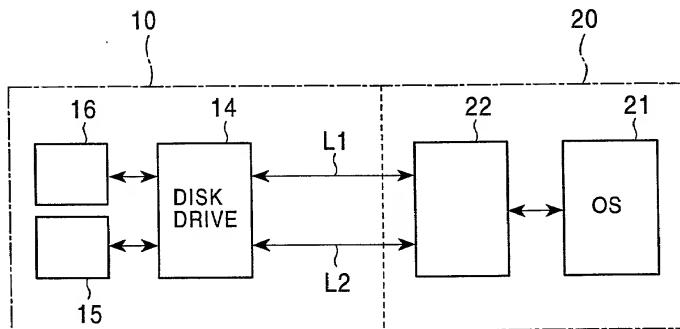


FIG. 3

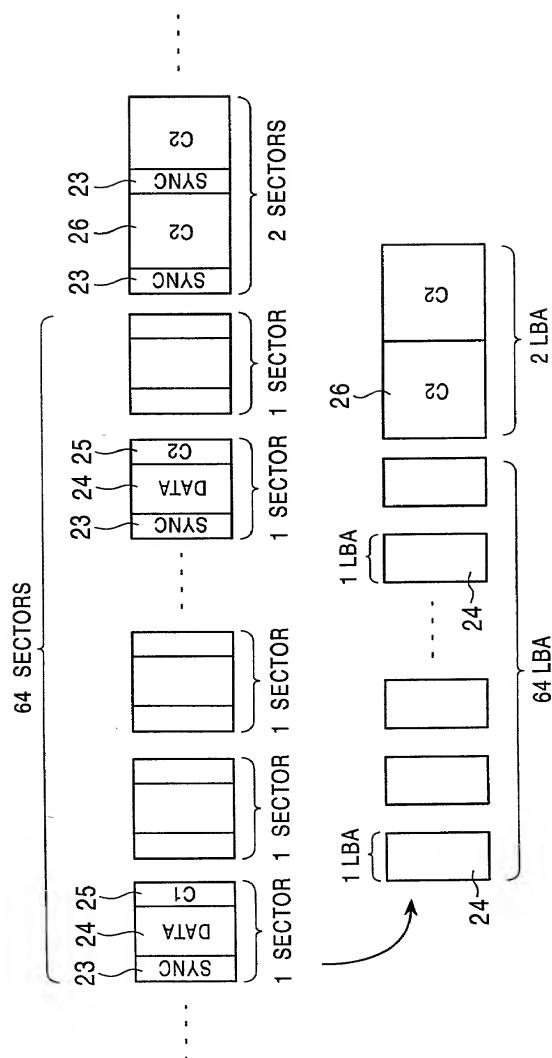
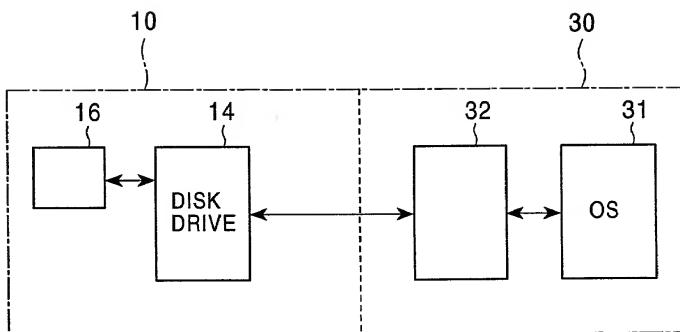


FIG. 4
PRIOR ART



Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

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I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

Disk Device with ImprovedError-Correction Capability

上記発明の明細書（下記の欄でX印がついていない場合は、本書に添付）は、

the specification of which is attached hereto unless the following box is checked:

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(該当する場合) に訂正されました。 was filed on _____
as United States Application Number or
PCT International Application Number
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_____ (If applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration
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私は、米国法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、米国以外の国の少なくとも一ヵ国を指定している特許協力条約365条(a)項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張とともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願

11-047937

(Number)
(番号)

Japan

(Country)
(国名)(Number)
(番号)(Country)
(国名)

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(Application No.)
(出願番号)(Filing Date)
(出願日)

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(Application No.)
(出願番号)(Filing Date)
(出願日)(Application No.)
(出願番号)(Filing Date)
(出願日)

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I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Not Claimed
優先権主張なし25/02/1999
(Day/Month/Year Filed)
(出願年月日)(Day/Month/Year Filed)
(出願年月日)

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)
(出願番号)(Filing Date)
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Japanese Language Declaration
(日本語宣言書)

委任状： 私は下記の発明者として、本出願に関する一切の手続を米特許商標局に対して遂行する弁護士または代理人として、下記の者を指名いたします。（弁護士、または代理人の氏名及び登録番号を明記のこと）

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

See Attachment A

書類送付先

Send Correspondence to:
Brinks Hofer Gilson & Lione
P.O. Box 10395
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(312) 321-4200

直接電話連絡先：（名前及び電話番号）

Direct Telephone Calls to: (name and telephone number)

唯一または第一発明者名	Full name of sole or first inventor Takayuki Sugawara		
発明者の署名	日付	Inventor's signature: Takayuki Sugawara Date: February 14, 2000	
住所	Residence Fukushima-ken, Japan		
国籍	Citizenship Japan		
私書箱	Post Office Address c/o ALPS ELECTRIC CO., LTD.		
	1-7 Yukigaya, Otsuka-cho, Ota-ku, Tokyo, Japan		
第二共同発明者	Full name of second joint inventor, if any Hiroki Hirashima		
第二共同発明者	日付	Second inventor's signature: Hiroki Hirashima Date: February 14, 2000	
住所	Residence Fukushima-ken, Japan		
国籍	Citizenship Japan		
私書箱	Post Office Address c/o ALPS ELECTRIC CO., LTD.		
	1-7 Yukigaya, Otsuka-cho, Ota-ku, Tokyo, Japan		

(第三以降の共同発明者についても同様に記載し、署名すること)

(Supply similar information and signature for third and subsequent joint inventors.)

第三共同発明者	Full name of third joint inventor, if any Isao Asano		
第三共同発明者 日付	Third inventor's signature Date  February 14, 2000		
住所	Residence Fukushima-ken, Japan		
国籍	Citizenship Japan		
私書箱	Post Office Address c/o ALPS ELECTRIC CO., LTD. 1-7 Yukigaya, Otsuka-cho, Ota-ku, Tokyo, Japan		
第四共同発明者	Full name of fourth joint inventor, if any Kyoichi Shirane		
第四共同発明者 日付	Fourth inventor's signature Date  February 14, 2000		
住所	Residence Kanagawa-ken, Japan		
国籍	Citizenship Japan		
私書箱	Post Office Address c/o SONY CORPORATION 7-35 Kitashinagawa, 6-chome, Shinagawa-ku, Tokyo, Japan		
第五共同発明者	Full name of fifth joint inventor, if any		
第五共同発明者 日付	Fifth inventor's signature Date		
住所	Residence		
国籍	Citizenship		
私書箱	Post Office Address		

ATTACHMENT A

Guy W. Shoup	26,805
Allan J. Sternstein	27,396
Gustavo Siller, Jr.	32,305
John C. Freeman	34,483
William F. Prendergast	34,699
Vita G. Conforti	39,639
Mark H. Remus	40,141
Steven G. Steger	40,185
Tadashi Horie	40,437
Joseph F. Helz	41,070
Jason C. White	42,223